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PATENT NUMBER and ISSUE DATE

U.S. **UTILITY** Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10037436	12/31/2001	7̶1̶1	305	2̶1̶8̶5	

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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH ☐	RESCIND ☐	
Foreign priority claimed		☐ yes ☐ no	ATTORNEY DOCKET NO
35 USC 119 conditions met		☐ yes ☐ no	
Verified and Acknowledged Examiners's intials			5038-184

TITLE : Memory bus termination with memory unit having termination control

U.S.DEPT. OF COMM /PAT & TM-PTO-436L(Rev 12-94)

NOTICE OF ALLOWANCE MAILED				CLAIMS ALLOWED		
		Assistant Examiner		**Total Claims**	**Print Claim for O.G**	
ISSUE FEE				**DRAWING**		
Amount Due	**Date Paid**			**Sheets Drwg.**	**Figs.Drwg.**	**Print Fig.**
			Primary Examiner			
☐ **TERMINAL DISCLAMER**		**PREPARED FOR ISSUE**	**Application Examiner**			

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